U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. MI22-1398 SERIAL NO. 09/536,037 LIST OF ART CITED BY APPLICANT
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If Appropriate Initial 6,518,122 02/03 AB 6,492,688 12/02 Ilg (ILG) AC 6,373,114 Jong et al. 257 AD 6,001,747 12/14/1999 Annapragada 2001/0019868 9/01 Gonzalez et al. AF 2001/0003064 601 687 AG 2003/0013311 1/03 Chang et al. AН ΑI ΑĴ ΑK FOREIGN PATENT DOCUMENTS Document Date Country Translation Number AN ΑÓ OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) "Silicon Processing for The VLSI Era"; Wolf, Stanley, Ph.D., Lattice Press 1986 TMT AT **EXAMINER** DATE CONSIDERED *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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